

#### CERTIFICATE OF MAILING 37 C.F.R. § 1.8

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I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Mail Stop: DD, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date below:

08/29/03

Kathy Manas

**PATENT** 

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

BRIAN C. BARNES GEOFFREY S. STRONGIN RODNEY W. SCHMIDT

Serial No.: 10/010,161

Filed: 11/13/2001

For: MEMORY MANAGEMENT SYSTEMS AND METHOD PROVIDING LINEAR ADDRESS BASED MEMORY ACCESS

**SECURITY** 

Group Art Unit: 2122

Examiner: Unknown

Atty. Dkt. No.: 2000.056700

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SEP 0 3 2003

Technology Center 2100

### INFORMATION DISCLOSURE STATEMENT

MAIL STOP: DD Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents required by 37 C.F.R. § 1.98(a)(2) are enclosed for the convenience of the Examiner.

In accordance with 37 C.F.R §§ 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

The present Information Disclosure Statement is being filed within three months of the filing date of this patent application or prior to the receipt of a first Official Action reflecting an examination on the merits, and hence is believed to be timely filed in accordance with 37 C.F.R § 1.97(b).

Moreover, in accordance with 37 C.F.R § 1.97(e)(1), Applicant hereby certifies that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the present statement, as evidenced by the date of the enclosed search report.

No fees are believed to be due in connection with the filing of this Information Disclosure Statement; however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these materials, the Assistant Commissioner is hereby authorized to deduct said fees from Advanced Micro Devices, Inc.'s Deposit Account No. 01-0365/2000.056700.

Applicant respectfully requests that the listed documents be made of record in the present case.

Respectfully submitted,

Mark W. Sincell Reg. No. 52,226

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Date: August 27, 2003

Agent for Applicants

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Form PTO-1449 (modified)		Atty. Docket No.	Serial No.	
		2000.056700	10/010,161	
P E List of Patents and Publication	s for Applicant's	Applicant		<u>.                                    </u>
THE STATE OF THE S	RE STATEMENT	Barnes et al.		
AUG 2 9 2003 E (Use several sheets if n		Filing Date:	Group:	
$\mathbf{x}_{i}$	ecessary)	November 13, 2001	2122	
TRADEMANT U.S. Patent Documents See Page 1	Foreign	Patent Documents	Other Art	
See Page 1	See Page 1		See Page 1	

## **U.S. Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
	A1	6,154,818	11/28/2000	Christie	711	163	
	A2	5,627,987	05/06/1997	Nozue et al.	395	410	
	A3	· · · ·	·			D	CEIVED
	A4	<u>.</u>					
	A5	:					SEP 0 3 2003
	A6		; ·			Tech	nology Center 210
	A7					100	,

## **Foreign Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1						
	B2						
	В3						

# Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation		
	C1	International PCT Search Report PCT/US02/28984 dated August 8, 2003		
	C2	INTEL CORPORATION, "Intel Architecture Software Developer's Manual, Volume 3: System Programming Guide, Chapters 2-4, 6, 14, annex B." Intel Ref. Manual XP002248376 (1996)		
	C3	Huck, J et al., "Introducing the IA-64 Architecture" IEEE Micro, IEEE Inc., New York, U.S> Vol. 20, No. 5, Sept. 2000 pp 12-23, XP000976142		

EXAMINER:	DATE CONSIDERED:
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EXAMINER: INITIAL IF REFERENCE CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.